

## PROGRAMME SPECIFICATION

Course Record Information	
Name and level of <b>final</b> & intermediate awards	<b>MEng Electronic Engineering</b> <b>MEng Electronic Engineering (Sandwich)</b> <b>BEng Honours Electronic Engineering</b> <b>BEng Honours Electronic Engineering (Sandwich)</b> BEng Electronic Engineering BEng Electronic Engineering (Sandwich) Diploma of HE in Electronic Engineering Certificate of HE in Electronic Engineering
Awarding Body	University of Westminster
Location of Delivery	University of Westminster, New Cavendish Street, Central London
Mode of Study	Full time
UW Course Codes	MEng: U09FMELE BEng: U09FUELE with Foundation. U09FFELE
JACS Code	H6
UCAS Codes	MEng: H611 BEng: H610 with Foundation. H608
QAA Subject Benchmarking Group	Engineering
Professional Body Accreditation	IET CEng
Date of initial course approval/last review	April 2006
Date of Programme Specification	Sept. 2010

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<b>Admissions Requirements</b>	
<p>Students who had their secondary education in the UK should have at least 5 GCSE passes at Grade C or equivalent including English Language and Mathematics. The University normally requires all undergraduate applicants who have not had their secondary education through the medium of English to attain the equivalent of IELTS 6.0, Cambridge Proficiency, or TOEFL 550 (paper)/80 (internet).</p> <p>As well as these, applicants should meet one of the requirements listed below:</p> <p><b>A-Level Entry</b></p> <p>At least two subjects passed in the General Certificate of Education (GCE) at Advanced Level, one of which must be Mathematics. Usually grades equivalent to at least BBB for MEng entry and BBC for BEng entry are required.</p> <p><b>Advanced Diploma Entry</b></p> <p>The award of an Advanced Diploma in Engineering. Usually, a Grade B, plus relevant Additional Specialist Learning (ASL) at Grade B for MEng entry or Grade C for BEng entry, would be required. The ASL must include either the OCR Certificate in Mathematics for Engineering or A level mathematics</p> <p><b>National Diploma Entry</b></p> <p>The award of a BTEC National Diploma or Certificate in Engineering. Usually, diploma grades of DDM for MEng entry and DMM for BEng entry will be required.</p> <p><b>Foundation Course Entry</b></p> <p>The award of a Certificate or Diploma upon completion of an approved foundation or access course.</p> <p><b>Other Entry</b></p> <p>Candidates holding qualifications differing in detail but not in standard from the above (e.g. an approved Secondary Leaving Certificate such as the International Baccalaureate with acceptable grades in relevant subjects) may be considered eligible for admission to the Course.</p> <p><b>Direct Entry to Level 5 (Year 2):</b> Candidates who have successfully completed studies comparable in content and standard to the Level 4 of the Course, including a practical skills component, may be considered for direct entry to the second year of the Course.</p> <p>Where possible, all applicants are interviewed and may be given an aptitude test.</p>	

<b>Aims of the courses</b>
<p>These courses have a strong focus on the <b>design</b> of electronic systems, based upon solid analytical foundations and the technical and interpersonal skills necessary to allow the graduate, to adapt to future technological developments. The emphasis is upon electronic <b>systems</b> rather than component devices.</p> <p>They are built on a common foundation that aims to provide an experience of the analysis and design of a variety of electronic systems including a sound knowledge of discrete and</p>

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<p>continuous-time signal processing, digital systems, communications systems, real-time embedded computer systems and analog circuit techniques This emphasises the strong interlinking between the themes before specialisation from the third year (fourth year in sandwich mode) onwards.</p> <p>These courses aim to:</p> <ul style="list-style-type: none"> <li>• Provide an exciting, enjoyable and rewarding learning experience which will serve as a solid foundation for a professional engineering career leading eventually to registration as a Chartered Engineer;</li> <li>• Establish fundamental principles of electronics, mathematics and computing, and develop the connection between these and a broad range of engineering systems;</li> <li>• Develop the analytical skills necessary to characterise and model circuits and systems in both continuous and discrete time;</li> <li>• Encourage initiative and confidence in approaching engineering problems and adoption of an investigative approach to their solution using a blend of analytical and practical skills;</li> <li>• Develop skills in presentation of technical work, the interpersonal and organisational requirements associated with carrying out an engineering project, and an appreciation of the industrial and social context of the technology;</li> <li>• Give an understanding of the role and responsibilities of the professional engineer to society and the environment;</li> <li>• Engender the communication and interpersonal skills necessary for operation in a professional engineering environment and to provide an education that allows graduates to adapt the future changes in technology.</li> </ul> <p>Pathways within the BEng programme aim to provide additional specialist knowledge and design skills:</p> <ul style="list-style-type: none"> <li>• The <b>Telecommunications Pathway</b> course aims to equip the student with the knowledge and skills necessary to design digital communication wireless systems.</li> <li>• The <b>Microelectronics Pathway</b> aims to equip the student with the knowledge and skills necessary to design digital and baseband analog circuits – particularly microelectronic integrated circuits.</li> </ul> <p>The MEng Electronic Engineering course additionally aims to both broaden and deepen the students' experience sharpening their ability to balance complex and sometimes conflicting requirements and constraints in the design of circuits and systems, involving a blend of digital and analog circuits underpinned with a sound knowledge of discrete and continuous-time signal processing.</p> <p>Pathways within the MEng programme aim to provide additional specialist knowledge and design skills:</p> <ul style="list-style-type: none"> <li>• The <b>Microelectronic Systems Pathway</b> additionally aims to provide specialisation in the design of mixed-signal as well as digital microelectronic systems on chip.</li> <li>• The <b>Embedded Systems Pathway</b> additionally aims to provide specialisation in the</li> </ul>	

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<p>design of embedded systems especially those with applications in audio and video signal processing.</p> <ul style="list-style-type: none"> <li>The <b>Wireless Systems Pathway</b> additionally aims to provide specialisation in the design of RF and microwave wireless circuits and systems.</li> </ul> <p>The supplementary aims of the <b>sandwich mode</b> of attendance are to provide graduates with relevant workplace experience and to launch their initial professional development with a view to becoming a Chartered Engineer.</p>	

<b>Employment and Further Study Opportunities</b>
<p>These degrees provide the ideal educational base for entry to a career not only in the electronics industry but also in any application with an electronic component. With an industry-wide shortage, high quality graduates are enjoying a choice of job opportunities with good salaries. This course provides a solid foundation for the design of a wide variety of electronic systems involving a blend of digital and analog circuits which use discrete or continuous-time signal processing techniques.</p> <p>Students on the Department's degree courses have gone on to work for the BBC and electronics giants such as BT, Nokia, British Aerospace and GEC, and to smaller private companies. Some have started up their own businesses in manufacturing or consultancy. Opportunities also exist for postgraduate study leading to PhD qualifications.</p>

<b>Learning Outcomes</b>
<p>Learning outcomes are statements on what successful students have achieved as the result of learning. They threshold statements of achievement and are linked to the knowledge, understanding and skills that a student will have gained on successfully completing a course.</p>
<p><b>Intermediate Learning Outcomes</b></p> <p>Level 4: students will be able to:</p> <ul style="list-style-type: none"> <li>read and create descriptions in words, mathematics or diagrams of simple electronics, software and mathematical concepts. They will build on their existing mathematical skills with techniques for the modelling of systems;</li> <li>design, implement and test simple analog and digital circuits, programs in high-level and low-level languages, and mathematical models of signal processing and communication systems.</li> <li>find information as directed, perform practical work given detailed instructions, work in groups with guidance from staff, and give short written and oral presentations on technical subjects.</li> </ul> <p>Level 5: students will be able to:</p> <ul style="list-style-type: none"> <li>be familiar with standard engineering and mathematical methods in the description,</li> </ul>

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analysis and interfacing of larger and more complex systems; <ul style="list-style-type: none"> <li>• use standard tools and methods to analyse and put together larger systems, in the specific areas of signal processing, communications, embedded systems, digital microelectronic and FPGA design, analog system design and object-oriented software.</li> <li>• work on structured group tasks, collaborating in the production of complex practical products and documentation</li> </ul>	
<h3>General Learning Outcomes</h3> <p>Graduates will satisfy the following criteria:</p> <p><b>Knowledge and Understanding:</b> they will be able to demonstrate their knowledge and understanding of essential facts, concepts, theories and principles pertaining to electronic engineering, and its underpinning science and mathematics. They will have an appreciation of the wider multidisciplinary engineering context and its underlying principles. They will appreciate the social, environmental, ethical, economic and commercial considerations affecting the exercise of their engineering judgement.</p> <p><b>Intellectual Abilities:</b> they will be able to apply appropriate quantitative science and engineering tools to the analysis of problems. They will be able to demonstrate creative and innovative ability in the synthesis of solutions and in formulating designs. They will be able to comprehend the broad picture and thus work with an appropriate level of detail.</p> <p><b>Practical skills:</b> they will possess practical engineering skills acquired through, for example, work carried out in laboratories and workshops; in industry through supervised work experience; in individual and group project work; in design work; and in the development and use of computer software in design, analysis and control.</p> <p><b>General transferable skills:</b> they will have developed transferable skills that will be of value in a wide range of situations. These skills include problem solving, communication, and working with others, as well as the effective use of general IT facilities and information retrieval skills. They also include planning self-learning and improving performance, as the foundation for lifelong learning/CPD.</p> <p>In addition, graduates with an MEng degree will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• The ability to develop, monitor and update a plan, to reflect a changing operating environment;</li> <li>• The ability to monitor and adjust a personal programme of work on an on-going basis, and to learn independently;</li> <li>• An understanding of different roles within a team, and the ability to exercise leadership;</li> <li>• The ability to learn new theories, concepts, methods, etc in unfamiliar situations.</li> </ul>	
<h3>Specific Learning Outcomes</h3> <p><b>1. Underpinning Science and Mathematics</b></p> <p>Graduates will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• Knowledge and understanding of scientific principles and methodology necessary to</li> </ul>	

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<p>underpin their education in electronic engineering, to enable appreciation of its scientific and engineering context, and to support their understanding of historical, current, and future developments and technologies;</p> <ul style="list-style-type: none"> <li>• Knowledge and understanding of mathematical principles necessary to underpin their education in electronic engineering and to enable them to apply mathematical methods, tools and notations proficiently in the analysis and solution of engineering problems;</li> <li>• Ability to apply and integrate knowledge and understanding of other engineering disciplines to support study of their own engineering discipline.</li> </ul> <p>In addition, graduates with an MEng degree will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• A comprehensive understanding of the scientific principles of own specialisation and related disciplines;</li> <li>• An awareness of developing technologies related to own specialisation;</li> <li>• A comprehensive knowledge and understanding of mathematical and computer models relevant to the engineering discipline, and an appreciation of their limitations;</li> <li>• An understanding of concepts from a range of areas including some outside engineering, and the ability to apply them effectively in engineering projects.</li> </ul> <p><b>2. Engineering Analysis</b></p> <p>Graduates will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• Understanding of engineering principles and the ability to apply them to analyse key engineering processes;</li> <li>• Ability to identify, classify and describe the performance of systems and components through the use of analytical methods and modelling techniques;</li> <li>• Ability to apply quantitative methods and computer software relevant to electronic systems in order to solve engineering problems;</li> <li>• Understanding of and ability to apply a systems approach to engineering problems.</li> </ul> <p>In addition, graduates with an MEng degree will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• Ability to use fundamental knowledge to investigate new and emerging technologies;</li> <li>• Ability to apply mathematical and computer-based models for solving problems in engineering, and the ability to assess the limitations of particular cases;</li> <li>• Ability to extract data pertinent to an unfamiliar problem, and apply in its solution using computer based engineering tools when appropriate.</li> </ul> <p><b>3. Design</b></p> <p>Graduates will have the knowledge, understanding and skills to:</p> <ul style="list-style-type: none"> <li>• Investigate and define a problem and identify constraints including environmental and sustainability limitations, health and safety and risk assessment issues;</li> <li>• Understand customer and user needs and the importance of considerations such as aesthetics;</li> </ul>	

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<ul style="list-style-type: none"> <li>• Identify and manage cost drivers;</li> <li>• Use creativity to establish innovative solutions;</li> <li>• Ensure fitness for purpose for all aspects of the problem including production, operation, maintenance and disposal;</li> <li>• Manage the design process and evaluate outcomes.</li> </ul> <p>In addition, graduates with an MEng degree will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• Wide knowledge and comprehensive understanding of design processes and methodologies and the ability to apply and adapt them in unfamiliar situations;</li> <li>• Ability to generate an innovative design for products, systems, components or processes to fulfil new needs.</li> </ul> <p><b>4. Economic, Social, and Environmental Context</b></p> <p>Graduates will be able to demonstrate:</p> <ul style="list-style-type: none"> <li>• Knowledge and understanding of commercial and economic context of engineering processes;</li> <li>• Knowledge of management techniques which may be used to achieve engineering objectives within that context;</li> <li>• Understanding of the requirement for engineering activities to promote sustainable development;</li> <li>• Awareness of the framework of relevant legal requirements governing engineering activities, including personnel, health, safety, and risk (including environmental risk) issues;</li> <li>• Understanding of the need for a high level of professional and ethical conduct in engineering.</li> </ul> <p><b>5. Engineering Practice</b></p> <p>Graduates will be able to demonstrate practical application of engineering skills, combining theory and experience, and use of other relevant knowledge and skills. This includes:</p> <ul style="list-style-type: none"> <li>• Knowledge of characteristics of protocols, equipment, processes, and products in the electronic and communication industries;</li> <li>• Workshop and laboratory skills;</li> <li>• Understanding of contexts in which engineering knowledge can be applied including operations and management, technology development, etc;</li> <li>• Understanding use of technical literature and other information sources;</li> <li>• Awareness of nature of intellectual property and contractual issues;</li> <li>• Understanding of appropriate codes of practice and industry standards;</li> <li>• Awareness of quality issues;</li> <li>• Ability to work with technical uncertainty.</li> </ul>	

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In addition, graduates with an MEng degree will be able to demonstrate: <ul style="list-style-type: none"> <li>• A thorough understanding of current practice and its limitations, and some appreciation of likely new developments;</li> <li>• Extensive knowledge and understanding of a wide range of engineering materials and components;</li> <li>• Ability to apply engineering techniques taking account of a range of commercial and industrial constraints.</li> </ul>	

<b>Teaching, Learning and Assessment Methods</b>
<p><b>Teaching and Learning Methods</b></p> <p>The fundamental principle underlying the teaching methods used on these courses is “learning by doing”. That is, in order to learn and understand the engineering skills and techniques required, students cannot just be told them or read about them - they need to practise them.</p> <p>The following teaching methods are used</p> <ul style="list-style-type: none"> <li>• Lecture / seminar sessions</li> <li>• Projects (group and individual)</li> <li>• Laboratories and computer-aided engineering</li> <li>• Problem sheets, investigations and design</li> <li>• On-line learning</li> </ul> <p>Lecturers provide written and verbal feedback on students’ work throughout the course. This may be individual or for the whole class.</p> <p>Most of the mathematics in these courses is taught within the engineering modules which use it. This means that students learn the mathematical theory and how it is applied at the same time, so as to make it more obviously relevant.</p> <p>Unlike some programmes with a wide choice of separate modules, these courses build on tightly interrelated themes. They have been designed to fit together, and it is vital that knowledge and skills feed across from one subject to another. Staff teaching the modules have experience across a range of engineering areas, and will expect students to develop the same without compartmentalising ideas.</p> <p><b>Assessment Strategy</b></p> <p>These modules share a common assessment strategy. As well as checking that students have met the learning outcomes of the module, assessment will, where possible and appropriate, be:</p> <ul style="list-style-type: none"> <li>• formative (helping students to learn);</li> <li>• rigorous (not easily copied, or otherwise passed without appropriate knowledge and skill);</li> <li>• challenging (requiring understanding, not just memorising of facts or mathematical tricks);</li> <li>• workplace relevant (the sort of tasks engineers might be judged on by an employer);</li> </ul>

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<ul style="list-style-type: none"> <li>• interesting (relevant to the application of the subject).</li> </ul> <p>Modules may have between one or two aspects of assessment making up the total mark. There are minimum marks for each aspect. This means, for example, that students cannot make up for a very poor exam mark by getting an excellent coursework mark nor can they depend on a good group mark, due to the efforts of other group members, to compensate for a very poor individual mark. A wide variety of assessment methods are used, including</p> <ul style="list-style-type: none"> <li>• In-class tests (making up the majority of coursework marks)</li> <li>• Group work</li> <li>• Laboratories</li> <li>• Viva-voce examinations</li> <li>• Formal examinations</li> <li>• Written reports</li> <li>• Presentations and posters</li> <li>• Computer-based quizzes and exercises</li> <li>• Design and implementation of hardware and software</li> <li>• Analysis, testing and modification of existing hardware or software</li> <li>• Participation in class activities such as question-and-answer sessions</li> </ul>	

<b>Course Structure</b>
This section shows the core and option modules available as part of the MEng and BEng courses and their credit value. Undergraduate students normally study 120 credits per year. Modules marked * are project-based.

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#### **Credit Level 4**

Code	Title	Status	Value
EECT405	Digital Systems	Core	15
EECT401	Computer Systems Project	Core	15
EECN410	Engineering Programming	Core	15
EEEL420	Electronics	Core	15
EEEL400	Engineering Principles	Core	15
EEEL405	Linear Systems Analysis	Core	15
EECN406	Communications and Networks	Core	15
EEEL415	Engineering Science and Mathematics	Core	15
EBSY400	Communication and Learning Skills		
1EAPP04	English for Academic Purposes (overseas only)		

Award of Certificate of Higher Education available

#### **Credit Level 5**

Code	Title	Status	Value
EECT520	Event-Driven and GUI Programming	Core	15
EECT510	Embedded Microprocessor System Project *	Core	15
EEEL515	Analog Electronic Design Project *	Core	15
EECT505	Microelectronic and FPGA System Design Project *	Core	15
EECT525	Professional Engineering Practice	Core	15
EEEL505	Electronic Circuits	Core	15
EEEL500	Signal and System Analysis	Core	15
EEEL510	Digital Communications	Core	15

Award of Diploma of Higher Education available

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### Credit Level 6 (BEng)

Code	Title	Status	Value
EEEL605	Digital Signal Processing Design	Core	15
EECT625	Industrial Management	Core	15
EECT698	Individual Project *	Core	30
	Plus <b>two core modules</b> according to pathway ‡:		
EEEL620	Analog Microelectronics (U)	Core	15
EECT630	Algorithm Realisation (U)	Core	15
EEEL615	Communication Networks (T)	Core	15
EEEL610	Wireless RF and Microwave Systems (T)	Core	15
	Plus <b>two option modules</b> according to pathway ‡:		
EECT600	Real-Time Embedded Systems (U, T)	Option	15
EECT610	Embedded Processor Architectures (U, T)	Option	15
EEEL615	Communication Networks (U)	Option	15
EEEL610	Wireless RF and Microwave Systems (U)	Option	15
EEEL620	Analog Microelectronics (T)	Option	15
EECT630	Algorithm Realisation (T)	Option	15
‡ T = Telecommunications Pathway U = Microelectronics Pathway			

Award of BEng available.  
Award of BEng (Hons) available.

### Credit Level 6 (MEng)

Code	Title	Status	Value
EEEL600	Advanced System Analysis and Design	Core	15
EEEL620	Analog Microelectronics	Core	15
EEEL610	Wireless RF and Microwave Systems	Core	15
EECT625	Industrial Management	Core	15
EECT685	Individual Project *	Core	30
	Plus <b>one core module</b> according to pathway ‡:		
EECT600	Real-Time and Embedded Systems (E)	Core	15
EECT610	Embedded Processor Architecture (M)	Core	15
EEEL615	Communication Networks (W)	Core	15
	Plus <b>one option module</b> according to pathway ‡:		
EECT600	Real-Time Embedded Systems (M, W)	Option	15
EECT610	Embedded Processor Architectures (E, W)	Option	15

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#### Credit Level 7 (MEng)

Code	Title	Status	Value
EECT799	Industrial Group Project *	Core	40
EEEL700	Multirate Signal Processing	Core	20
	Plus <b>two core modules</b> according to pathway ‡:		
EECT710	SoC and FPGA Design Project (E, M)	Core	20
EECT705	Video & Image Processing (E)	Core	20
EEEL705	DSP & Communication Processor Design (M)	Core	20
EEEL715	Mobile & Wireless Communication Systems (W)	Core	20
EEEL720	Wireless System Design (W)	Core	20
	Plus <b>one option module</b> from below:		
EECT700	Embedded Media Processing	Option	20
EECT705	Video & Image Processing	Option	20
EECT710	SoC and FPGA Design Project	Option	20
EEEL705	DSP & Communication Processor Design	Option	20
EEEL710	Mixed-Signal Integrated Circuit Design	Option	20
EEEL715	Mobile & Wireless Communication Systems	Option	20
EEEL720	Wireless System Design	Option	20
‡	W = Wireless Systems Pathway E = Embedded Systems Pathway M = Microelectronic Systems Pathway		

Award of MEng available.

#### Assessment of Modules (Levels 4-6)

A *pass* in a module is achieved when the overall mark is at least 40% and the marks for separate aspects of assessment are individually at least 30% (for coursework/exam aspects) or at least 35% (for group-work/individual-work aspects).

At Level 4 only, a student who has failed certain elements of assessment may be awarded *condoned credit* in a module where he/she has achieved:

- (a) an overall module mark of greater than or equal to 30% but less than 40%;
- (b) an overall mark of 40% or greater but not reached the required standard in one or more aspect of assessment.

Where a student is awarded condoned credit, the recorded module mark will be capped at 40%. To be awarded a condoned credit the failed elements of assessment must have been attempted

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at *both* the first and referred opportunity.

Where a student is awarded condoned credit in a module but subsequently achieves an overall pass at a re-attempt, credit may contribute only once to an award.

Condoned credit cannot be awarded for modules at levels 5 or 6.

### Assessment of Modules (Level 7)

Assessment at Level 7 is both criterion-based and aggregate-based. For each module, the student must both obtain the threshold mark in each aspect of assessment and achieve *all* the learning outcomes of the module.

The learning modules have two aspects of assessment: the deliverables of the Independent Learning Package (ILP) and a viva voce examination. In order to *pass* a learning module at Level 7 the student must:

- (a) obtain a mark of at least 50% in the ILP deliverables;  
AND
- (b) obtain a mark of at least 50% in the viva voce examination;  
AND
- (c) demonstrate achievement in **each** and **every** learning outcome either through the ILP deliverables or during the viva voce examination.

### Progression Requirements

The University has regulations that govern the progression through the course.

In order to progress to Level 5, a student must obtain a minimum of 90 credits passed (i.e. not condoned) at Level 4 or above with an average of at least 40% across 120 credits.

In order to progress to Level 6 of the BEng course, a student must obtain a minimum of 195 credits at Level 4 or above, including a minimum of 75 credits at Level 5 or above.

In order to progress to Level 6 of the MEng course (or transfer from Level 5 of the BEng to Level 6 of the MEng course), a student must normally obtain a minimum of 120 credits at Level 5. In addition, the average mark obtained from the best modules worth 120 credits at Level 5 must normally be at least 60%.

In order to progress from Level 6 to Level 7 of the MEng course, a student must obtain a minimum of 120 credits at Level 6.

A student cannot normally attempt any module at the next level until they have fulfilled the progression requirement to that level. In addition, specific prerequisites and co-requisites have to be achieved in order to study each individual module at Credit Levels 5, 6 and 7.

### MEng Award

In respect of the modules described in this course scheme, to qualify for the award of **MEng Electronic Engineering** a student must:

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<p>(a) have obtained at least 480 credits including:</p> <ul style="list-style-type: none"> <li>(i) a minimum of 120 credits at Level 4 or higher, of which no more than 15 shall be condoned; and</li> <li>(ii) a minimum of 120 credits at Level 5 or higher, and</li> <li>(iii) a minimum of 120 credits at Level 6 or higher, including the Individual Project, and</li> </ul> <p>[Note: for IET accreditation, the Individual Project credits must be obtained without retake.]</p> <ul style="list-style-type: none"> <li>(iv) a minimum of 120 credits at Level 7 or higher, including the Industrial Group Project.</li> </ul> <p>(b) have attempted modules worth no more than 470 credits at Levels 5, 6 and 7. (An attempt includes a first attempt and any subsequent retake of any module but does not include reassessment without attendance.)</p> <p>An overall average mark for Levels 6 and 7 is calculated with a weighting of 40% for the best modules worth 120 credits at Level 6 and a weighting of 60% for the best modules worth 120 credits at Level 7. The MEng may be awarded with <b>Distinction</b> if this overall mark is at least 70% <i>else</i> with <b>Merit</b> if this overall mark is at least 60% <i>else</i> an MEng degree if this overall mark is at least 50%.</p> <p>A student enrolled on the MEng course who fails to fulfil the requirements to be awarded an MEng degree (or who wishes) may instead be considered for the award of BEng (Honours) degree or BEng degree in accordance with the regulations for those courses. In such cases, credits and marks awarded for Level 6 MEng modules may substitute for Level-6 BEng modules. Other substitutions of Level 7 modules for Level-6 BEng modules may be made at the discretion of the Conferment Board.</p> <p><b>BEng Honours Award</b></p> <p>In respect of the modules described in this course scheme, to qualify for the award of <b>BEng Honours Electronic Engineering</b> a student must:</p> <p>(a) have obtained at least 360 credits including:</p> <ul style="list-style-type: none"> <li>(i) a minimum of 120 credits at Level 4 or higher, of which no more than 15 shall be condoned; and</li> <li>(ii) a minimum of 120 credits at Level 5 or higher, and</li> <li>(iii) a minimum of 120 credits at Level 6 or higher, including the final-year Individual Project, and</li> </ul> <p>[Note: for IET accreditation, the Individual Project credits must be obtained without retake.]</p> <p>(b) have attempted modules worth no more than 330 credits at Levels 5 and 6. (An attempt includes a first attempt and any subsequent retake of any module but does not include reassessment without attendance.)</p>	

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<p><b>Honours Classification:</b> The class of degree will normally be determined as follows:</p> <p>First Class: An average of 70% or higher in the best modules worth 120 credits at Level 6, with an average of at least 60% in the best modules worth 120 credits remaining at Levels 5 and 6.</p> <p>Upper Second Class: An average of 60% or higher in the best modules worth 120 credits at Level 6, with an average of at least 50% in the best modules worth 120 credits remaining at Levels 5 and 6.</p> <p>Lower Second Class: An average of 50% or higher in the best modules worth 120 credits at Level 6, with an average of at least 40% in the best modules worth 120 credits remaining at Levels 5 and 6.</p> <p>Third Class: An average of 40% or above in the best 240 credits at Levels 5 and 6.</p> <p>To achieve the award of <b>BEng Honours Electronic Engineering (Sandwich)</b>, the conditions for the corresponding named degree must be fulfilled plus the industrial placement must have been assessed as successfully completed.</p> <p>The classification of the sandwich degree will be determined by the same criteria as for the corresponding named degree. The industrial placement will not contribute to the classification.</p> <p><b>Intermediate Awards</b></p> <p><b>Non-Honours BEng Degree:</b> In respect of the modules described in this course scheme, to qualify for the award of <b>BEng Electronic Engineering</b> a student must:</p> <p>(a) have obtained at least 300 credits including:</p> <ul style="list-style-type: none"> <li>• a minimum of 120 credits at Level 4 or higher, of which no more than 15 shall be condoned; and</li> <li>• a minimum of 120 credits at Level 5 or higher, and</li> <li>• a minimum of 60 credits at Level 6 or higher, and</li> </ul> <p>(b) have achieved a mark of at least 35% in the final-year project module.</p> <p>The BEng Electronic Engineering may be awarded with <b>Merit</b> to a student whose marks average at least 60% across the best 150 credits at Credit Levels 5 and 6;</p> <p>The BEng Electronic Engineering may be awarded with <b>Distinction</b> to a student whose marks average at least 70% across the best 150 credits at Credit Levels 5 and 6.</p> <p><b>Diploma of Higher Education:</b> In respect of the modules described in this course scheme, to qualify for the award of <b>DipHE in Electronic Engineering</b> a student must have obtained at least 240 credits including:</p> <ul style="list-style-type: none"> <li>• a minimum of 120 credits at Level 4 or higher, of which no more than 15 shall be condoned; and</li> <li>• a minimum of 120 credits at Level 5 or higher.</li> </ul> <p>The DipHE Electronic Engineering may be awarded with <b>Merit</b> to a student whose marks average at least 60% across the best 105 credits at Credit Level 5 or higher;</p> <p>The DipHE Electronic Engineering may be awarded with <b>Distinction</b> to a student whose marks</p>	

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<p>average at least 70% across the best 105 credits at Credit Level 5 or higher.</p> <p><b>Certificate of Higher Education:</b> In respect of the modules described in this course scheme, to qualify for the award of <b>CertHE in Electronic Engineering</b> a student must have obtained a minimum of 120 credits at Level 4 or higher, of which no more than 15 shall be condoned.</p> <p>The CertHE Electronic Engineering may be awarded with <b>Merit</b> to a student whose marks average at least 60% across the best 105 credits at Credit Level 4 or higher.</p> <p>The CertHE Electronic Engineering may be awarded with <b>Distinction</b> to a student whose marks average at least 70% across the best 105 credits at Credit Level 4 or higher.</p>	

Support for Students
<p>On arrival, an induction programme will introduce students to the staff responsible for the course, the campus on which they will be studying, the Library and IT facilities and to the Campus Administration. Students will be provided with the Course Handbook, which provides detailed information about the course. Students are allocated a personal tutor who can provide advice and guidance on academic matters.</p> <p>Learning support includes the Library which, across its four sites, holds printed collections of 412,000 books, 1,600 journal subscriptions and substantial audio visual collections. Access to over 6,500 electronic resources (databases, e-journals, e-books, exam papers and links to recommended websites) is facilitated through infoLinX, the library portal.</p> <p>There are over 3,500 computers spread over the four University campuses available for students use. The University uses a Virtual Learning Environment called Blackboard where students can access course materials and communicate with staff and other students via message boards.</p> <p>At University level, Services for Students provide advice and guidance on accommodation, financial and legal matters, personal counselling, health and disability issues, careers and the chaplaincy providing multi-faith guidance. The International Education Office provides particular support for international students. The University of Westminster Students' Union also provides a range of facilities to support all students during their time at the University.</p>

Reference Points for the course
Internally <ul style="list-style-type: none"> <li>• University Quality Assurance Handbook and Modular Frameworks</li> <li>• Staff research and development in Electronics and Communications</li> </ul>

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<ul style="list-style-type: none"> <li>• Industrial advisory panel</li> </ul> <p>Externally</p> <p>Mainly:</p> <ul style="list-style-type: none"> <li>• UK-SPEC (Engineering Council's UK Standard for Professional Engineering Competence) The Accreditation of Higher Engineering Programmes</li> <li>• IET (Institution of Engineering and Technology) Academic Accreditation Guidelines</li> <li>• QAA Subject Benchmark for Engineering</li> </ul> <p>Also:</p> <ul style="list-style-type: none"> <li>• EPC (Engineering Professors' Council) The EPC Engineering Graduate Output Standard</li> <li>• QAA Guidelines for Preparing Programme Specifications</li> <li>• SEEC Credit Level Descriptors for Further and Higher Education</li> </ul>	
<b>Quality Management and Enhancement</b>	
<p><b>Course Management</b></p> <p>This course is managed by staff from the Department of Electronics, Networks and Computer Engineering (ENCE) in the School of Electronics and Computer Science (ECS). The Course Team consists of lecturers on individual modules, the Head of Department and technical support staff. The day-to-day running of each course is the responsibility of the Course Leader, while the strategic direction of the courses and the allocation of staff is the responsibility of the Head of the Department. The Dean of the School of Electronics and Computer Science takes overall responsibility for all departments within this School.</p> <p><b>Course approval, monitoring and review</b></p> <p>This course has been developed from and built upon similar courses previously approved by University Validation Panels. The Panels included internal peers from the University and external subject specialists from academia and industry to ensure the comparability of the courses to those offered in other Universities and the relevance to employers. Quinquennial Course Reviews help to ensure that the curriculum is up-to-date and that the skills gained on the courses continue to be relevant to employers.</p> <p>Our courses are monitored each year by the School of Electronics and Computer Science to ensure that they are running effectively and that issues that might affect the student experience have been appropriately addressed. Staff will consider the outcomes from the Course Committee, evidence of student progression and achievement and the reports from External Examiners to evaluate the effectiveness of the course. The Campus Academic Standards Group audits these processes and the outcomes are reported to the Academic Council of the</p>	

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<p>University, which has overall responsibility for the maintenance of quality and standards in the University.</p> <p><b>Student involvement in Quality Assurance and Enhancement</b></p> <p>Student feedback is important to the University and student comment is taken seriously. The most formal mechanism for feedback on the course is the course committee. Student representatives are elected to sit on the committee to represent the views of their peer group in the discussions held at the committee. The University and the Students' Union work together to provide a full induction to the role of the Course Committee.</p> <p>Students are asked to complete an end-of module questionnaire at the end of each module. The feedback from this informs the Module Leader on the effectiveness of the module and highlights areas that could be enhanced.</p> <p>Students meet with Review Panels when the periodic review of the course is conducted to provide oral feedback on their experience on the course. Student meetings are also held on an annual basis with representatives of the School Academic Standards Group as part of the annual monitoring process.</p>	
<p>For more information about this course:</p> <p>Admissions tutor:       Dr Mohammed Al-Janabi  Course leader:            Dr Viv Bartlett  Web site:    <a href="http://www.westminster.ac.uk/schools/computing/subjects/electronic-engineering">http://www.westminster.ac.uk/schools/computing/subjects/electronic-engineering</a></p>	
<p>Please note – This programme specification provides a concise summary of the main features of the course and the learning outcomes that a student might reasonably be expected to achieve and demonstrate if s/he takes full advantage of the learning opportunities that are provided. This specification should be read in conjunction with the Course Handbook provided to students and Module Handouts which provide more detailed information on the specific learning outcomes, content, teaching, learning and assessment methods for each module.</p>	